

Google scholar

firewall processing control unit microchip OR n

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

Articles excluding patents ▾

1970

- 2000

include citations ▾

Results 1 - 20 of about 120. (0.92 sec)

[CITATION] Trace processors: Moving to fourth-generation microarchitectures

JE Smith, S Vajapeyam - Computer, 1997

[Cited by 86](#) - [Related articles](#) - [BL Direct](#) - [All 6 versions](#)

[CITATION] ... for efficient protocol **processing** in highspeed communication ...

G Konstantoulakis, V Nellas, C Georgopoulos, T ... - ... Networks, 2000. ECUMN ..., 2000

[Cited by 7](#) - [Related articles](#)

[CITATION] Adaptive Sequential Diagnosis Algorithm by Point to Point Network

X Jianchi, Z Dafang - JOURNAL OF NATURNAL SCIENCE OF ..., 1999 - en.cnki.com.cn

... Hefei 230601,China);Analysis and realization of the audit of **firewall**[J];Journal of ... Chongqing 400044,China);Fault-tolerant and Its Application in **Process Control**[J];JOURNAL ... Electric Locomotive Research Institute(Zhuzhou 412001) Zhou Guifa;Microcomputer **control** system of ...

[Related articles](#) - [Cached](#)

The stanford flash multiprocessor

J Kuskin, D Ofeit, M Heinrich, J Heinlein, R ... - 25 years of the ..., 1998 - portal.acm.org

... MAGIC provides a hardware-based "**firewall**" mechanism that can be used to prevent certain operations (memory writes ... Figure4.3 shows the structure of the **control** mac- to&reline. ... to the flexible controller - the protocol pro- cessor (IT) - where the actual protocol **processing** occurs ...

[Cited by 768](#) - [Related articles](#) - [BL Direct](#) - [All 33 versions](#)

[psu.edu \[PDF\]](#)

[CITATION] Challenges and opportunities in broadband and ...

JM Rabaey, M Potkonjak, F Koushanfar, SF Li, T Tuan - IEEE/ACM International ..., 2000

[Cited by 23](#) - [Related articles](#) - [BL Direct](#) - [All 27 versions](#)

[psu.edu \[PDF\]](#)

[CITATION] Remote monitoring of circuit breakers

JC Engel, WJ Murphy, DM Oravetz - ... Conference, 1999. Thirty-Fourth IAS Annual ..., 1999

[Cited by 1](#) - [Related articles](#) - [BL Direct](#)

[CITATION] Economic online self-test in the time-triggered architecture

A Steiringer, C Temple - IEEE Design & Test of Computers, 1999

[Cited by 12](#) - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)

Multimedia--An introduction

RJ Flynn, WH Tetzlaff - Journal of Research, 1998 - [dx.doi.org](#)

... Entirely new techniques are required, and the human factors involved in the **processing** of this new ... The STB has a remote-**control unit**, which allows the viewer to make choices from a distance. ...

Another issue in Internet architecture is the role of **firewalls**, which are used at the ...

[Cited by 17](#) - [Related articles](#) - [BL Direct](#) - [All 9 versions](#)

[PDF] Bringing the internet to all electronic devices

M Howard, CS Sontag - Proceedings of the Workshop on ..., 1999 - [sagecertification.org](#)

... Device **Firewall**. ... of the ancillary network or device, or the device connection is required to periodically contact the gateway **process**, the gateway ... A home management software package, which could run on a PDA or browser, may access a heating **control unit** based on the ...

[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 5 versions](#)

[sagecertification.org](#) [PDF]

[PDF] in safety PLC embedded software

WM Goble - 2000 - [safetybase.net](#)

... **FIREWALLS** AROUND CRITICAL FUNCTIONS When safety critical functions must be combined with non-safety critical functions, the design must include sufficient safeguards for non ... Any quality **control** effort's goal is to find the root cause and fix the **process** in an irreversible ...

[Related articles](#) - [View as HTML](#) - [All 4 versions](#)

[safetybase.net](#) [PDF]

[BOOK] Computer fundamentals: architecture and organization

B Ram - 2000 - [books.google.com](#)

... Circuits 4.50 4.5 Arithmetic Circuits 4.57 PROBLEMS 4.58 CHAPTERS CENTRAL **PROCESSING UNIT** (CPU) 5.1 ... of Intel 8085 5.76 5.7 Generation of Some Special **Control** Signals for ... 5.55 5.23 PA 800 Series of Microprocessors 5.55 5.24 PowerPC **Microprocessor** 5.59 5.25 ...

[Cited by 3](#) - [Related articles](#) - [All 2 versions](#)

MMLite: a highly componentized system architecture

J Helander, A Forin - Proceedings of the 8th ACM SIGOPS ..., 1998 - [portal.acm.org](#)

... editing an Excel spreadsheet inside a Word document it is in fact the Excel **process** that operates on ... The virtual memory system implements **firewalls** between applications. ... A driver for the Interrupt **Control Unit** (ICU) dispatches interrupts and keeps a registry of interrupt routines ...

[Cited by 84](#) - [Related articles](#) - [All 14 versions](#)

[psu.edu](#) [PDF]

[BOOK] Computer Integrated Manufacturing

MSG Prasad, P MacConalli, BS Raju - 1988 - books.google.com

... Inventory **control** is to ensure the stock of the goods for the production of the component. ... 5. WIP (Work in **process**) 1. Production Rate The production rate for an individual **processing** or assembly ... Operating cycle time: It is defined as the time that one work **unit** is being processed ...

[CITATION] Hardware based error and flow **control** in the Axon gigabithost-network ...

JPG Sterbenz, A Kantawala, MM Buddhikot, GM ... - IEEE INFOCOM'92 ..., 1992

[Cited by 1](#) - [Related articles](#) - [All 4 versions](#)

The VMP network adapter board (NAB): High-performance network communication ...

H Kanakia, D Cheriton - ACM SIGCOMM Computer Communication ..., 1988 - portal.acm.org

... the pipeline at the host-end precludes strict implementation of a network **firewall**; data is ... Adopting a common packet format shown in Figure 3, and using programmable **control**, the pipeline ... The cost of **processing** per- formed by the NAB processor seems to be equally shared ...

[Cited by 161](#) - [Related articles](#) - [All 3 versions](#)

Securing ATM networks

SC Chuang - Proceedings of the 3rd ACM conference on Computer ..., 1996 - portal.acm.org

... VC), then there is literally no difference between an ap- plication layer **firewall** and network **firewall**. ...

A security re- lated data **unit** is passed transparently from the device driver and is ... These DAN devices have different degrees of intelligence in terms of **control processing** power. ...

[Cited by 35](#) - [Related articles](#) - [BL Direct](#) - [All 13 versions](#)

[psu.edu \[PDF\]](#)

[PDF] Add Internet Appliance Technology to Automatic Test Equipment

EF Steinfeld, ECM Consultant - DEDICATED SYSTEMS ..., 2000 - dedicated-systems.net

... Primary protection against outside intruders is provided by the corporate **firewall**. ... Starting with a stand- alone machine controller or instrument (See Figure 3) add the **processing** power, extra ... server software to allow the use of remote Web browsers to display and **control** data to ...

[View as HTML](#) - [BL Direct](#) - [All 13 versions](#)

[dedicated-systems.net \[P](#)

Providing policy-neutral and transparent access **control** in extensible systems

R Grimm, B Bershad - Secure Internet Programming, 1999 - Springer

... distinct and concise de- scription of the abstractions found in a **unit** of code. ... This **process** of combining functionality and security to provide access **control** in an extensible ... Furthermore, benchmarks that perform access **control** checks use simple permissions instead of permission ...

[Cited by 37](#) - [Related articles](#) - [BL Direct](#) - [All 22 versions](#)

[psu.edu \[PDF\]](#)

Advanced tool kits for EPR security

B Blobel - International journal of medical informatics, 2000 - Elsevier

... However, domain-related mechanisms such as **firewalls** might be introduced, therefore, also within ... specific models and standards specifying authorisation and access **control** can easily ... 1.

ISO/IEC, Information Technology-Open Distributed **Processing**-Reference Model, Part 2 ...

[Cited by 10](#) - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

[CITATION] A case for NOW (Networks of Workstations)

TE Anderson, DE Culler, D Patterson - IEEE micro, 1995

[Cited by 944](#) - [Related articles](#) - [BL Direct](#) - [All 86 versions](#)

[psu.edu \[PDF\]](#)

Gooooooooogle ►

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) **[Next](#)**

firewall processing control unit micro **Search**

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google

Google scholar

firewall "processing unit" "control unit" microch

Search

[Advanced Scholar Search](#)

[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

Articles excluding patents ▾

1970

- 2000

include citations ▾

Results 1 - 2 of 2. (0.09 sec)

[BOOK] [Computer fundamentals: architecture and organization](#)

B Ram - 2000 - [books.google.com](#)

... 1.3 MAJOR COMPONENTS OF A DIGITAL COMPUTER The major components of a digital computer are: CPU (central **processing unit**), memory, input device and output device. ... The CPU of a small computer is a **microprocessor**. Fig. ...

[Cited by 3](#) - [Related articles](#) - [All 2 versions](#)

[BOOK] [Hardware-Software-Co-Design of a Time-Triggered Protocol](#)

G Stöger - 1997 - [vmars.tuwien.ac.at](#)

... The Communication Network Interface (CNI) constitutes a temporal **firewall** which protects the communication system from timing errors in the application processor and vice versa, requiring the generation of schedules for independent communication and host systems. ...

[Related articles](#) - [View as HTML](#)

[tuwien.ac.at \[PDF\]](#)

firewall "processing unit" "control uni Search

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

Google scholar

network "access control" "control unit" microch Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

[Articles excluding patents ▾](#)
[1970](#) - [2000](#)
[include citations ▾](#)

Results **1 - 20** of about **397**. (0.66 sec)

[CITATION] Network control for wireless communications

DJ Goodman, GP Pollini, KS Meier-Heilster - IEEE Communications Magazine, 1992

[Cited by 49](#) - [Related articles](#) - [BL Direct](#) - [All 2 versions](#)

A reservation principle with applications to the ATM traffic control

PE Boyer, DP Tranchier - Computer Networks and ISDN Systems, 1992 - Elsevier

... They are non-confor- mance-proof since user and general **network** are decoupled by an FRP

Control Unit and only ... The Reservation Principle introduces short- term reliable prediction of the multiplex load which in turn allows for an efficient burst **access control**: if a ...

[Cited by 196](#) - [Related articles](#) - [All 4 versions](#)

[CITATION] An overview of backscattered radio frequency identification system (...

KVS Rao - Microwave Conference, 1999 Asia Pacific, 1999

[Cited by 101](#) - [Related articles](#)

... and analysis of the error characteristics of an in-building wireless **network**

D Eckhardt, P Steenkiste - Conference proceedings on Applications, ..., 1996 - portal.acm.org

... a large space,we believeit is worthy of studybecause its Medium **Access Control** (MAC) protocol

and ... Products include ISA and PCMCIA **network** interfaces for PC-compatible computers and

stand-alone ... The modem **control unit** prepends a 16-bit "**network ID**" to ev- ery packet on ...

[Cited by 209](#) - [Related articles](#) - [BL Direct](#) - [All 35 versions](#)

[psu.edu \(PDF\)](#)

Distributed storage **control unit** for the Hitachi S-3800 multivector supercomputer

K Kitai, T Isobe, T Sakakibara, S Yazawa, Y ... - Proceedings of the ..., 1994 - portal.acm.org

... memory **access control** in the V-SCS and M-SCS, This also enhances the indirect memory ... Cray

Y-MP series uses a muMi- stage crossbar **network** [10], Although this **network** requires more ... up

to four scalar and vector processors (SPS and VPS), a storage **control unit** (SC), main ...

[Cited by 20](#) - [Related articles](#) - [All 2 versions](#)

A systematic approach to the design of digital bussing structures

KJ Thurber, ED Jensen, LA Jack, LL Kinney, ... - Proceedings of the ..., 1972 - portal.acm.org

... NA Boehmer et al Advanced avionic digital computer---arithmetic and **control unit** design Hughes ...
56. M1 JS Mayo An approach to digital system **network** IEEE Transactions on ... Specifically described
are: message structure, **access control**, error checking and required bandwidth. ...

[Cited by 62](#) - [Related articles](#) - [All 3 versions](#)

Dynamic scheduling in cellular manufacturing systems: a framework for networked ...

MJ Shaw - Journal of Manufacturing Systems, 1988 - Elsevier

... the Ethernet developed by Xerox;' and (2) the token passing scheme, which is the **access control**
scheme incor ... set up of a flexible cell or a robot assembly cell, the cell's **control unit** would give ...
the CNC machines³⁵ When the cell is assigned to a job by the **network** wide bidding ...

[Cited by 79](#) - [Related articles](#)

... method based on coloured petri net models applied on an **access control** system

K Mortensen - Application and Theory of Petri Nets 2000, 2000 - Springer

... This command creates an MS DOS executable, mosmlout.exe, which now can be started on the
central **control unit**, assuming the LonWorks **network** is ... LonWorks **network** ... The post-**processing**
tool does not depend on the LonTalk library as used with the **access control** model. ...

[Cited by 17](#) - [Related articles](#) - [BL Direct](#) - [All 4 versions](#)

Multiprocessor organization—A survey

P Enslow Jr - ACM Computing Surveys (CSUR), 1977 - portal.acm.org

... Vocabulary for Informa- tion **Processing** as "a computer employing two or more **processing** units
under inte ... With respect to the hardware, a multi- **processor** must have the capability for the direct ...
of main memory by all proc- essors (arithmetic/logic unit and **control unit** only) and ...

[Cited by 136](#) - [Related articles](#) - [All 4 versions](#)

[CITATION] Experimental evaluation of a wireless ad hoc **network**

S Desilva, SR Das - ... and Networks, 2000. Proceedings. Ninth International ..., 2000

[Cited by 56](#) - [Related articles](#) - [All 12 versions](#)

[psu.edu \[PDF\]](#)

A distributed scheduling method for computer integrated manufacturing: the use of ...

MJ Shaw - International Journal of Production Research, 1987 - informaworld.com

... (d) The **access control** scheme is ... the cell's **control unit** would give different performance estimates
at different moments. ... Ridding When a cell receives a task-announcement message from the
communication **network**, it first matches the task description with its capability-list and ...

[Cited by 60](#) - [Related articles](#) - [All 5 versions](#)

[CITATION] The Batcher-banyan self-routing **network**: universality and simplification

MJ Narasimha - IEEE Transactions on communications, 1988

[Cited by 43](#) - [Related articles](#) - [All 4 versions](#)

[CITATION] Distributed motion control using real-time **network** ...

SY Lin, CY Ho, YY Tzou - Power Electronics and Motion Control Conference, ..., 2000

[Cited by 8](#) - [Related articles](#)

[CITATION] Cryptology for digital TV broadcasting

BM Macq, JJ Quisquater - Proceedings of the IEEE, 1995

[Cited by 305](#) - [Related articles](#) - [BL Direct](#) - [All 4 versions](#)

[cuny.edu \[PDF\]](#)

Principle for high speed **network** control: congestion-and deadlock-freeness, self- ...

Y Ofek, M Yung - Proceedings of the ninth annual ACM symposium ..., 1990 - portal.acm.org

... of the switch is larger than the sum of the bandwidths of all the **processor**-input links ... Buffer insertion is an access technique to a unidirectional ring **network**, which is random and ... Clearly, the buffer insertion **access control** may enable the concurrent access (or spatial reuse) of the ...

[Cited by 12](#) - [Related articles](#) - [All 2 versions](#)

The architecture of a Gb/s multimedia protocol adapter

E Rüttsche - ACM SIGCOMM Computer Communication Review, 1993 - portal.acm.org

... Media **Access Control Unit** (MACU) : The MPA is designed to be connected to any high-speed net- work. ... interface to the Aurora ATM **network** , ... The Protocol **Processor** T9000 : The selection of the inmos2 T9000 [inmos 91] is based on our good experience with the transputer ...

[Cited by 14](#) - [Related articles](#) - [BL Direct](#)

[CITATION] Interprocess communication for distributed robotics

D Gauthier, P Freedman, G Carayannis, A Malowany - Robotics and Automation, ..., 1987

[Cited by 34](#) - [Related articles](#) - [All 3 versions](#)

[CITATION] Modeling an ATM-based access **network** for 3rd generation ...

M Boldt, U Weiss, R Sigle, U Dropmann - 48th IEEE Vehicular Technology ..., 1998

[Cited by 7](#) - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)

[129.69.170.1 \[PDF\]](#)

Arrow: A flexible architecture for an accounting and charging infrastructure in the ...

G Fankhauser, B Stiller, B Plattner - Netnomics, 1999 - Springer

... outlines the mechanisms required for implementing the reservation and corre- sponding **access control** functions. ... for testing and implementing charging and accounting in an IPv6 based **network** and provides ... engine, as depicted in figure 4(a), offers a module **control unit** and an ...

[Cited by 36](#) - [Related articles](#) - [All 12 versions](#)

[psu.edu \[PDF\]](#)

Mark IIIfp hypercube concurrent **processor** architecture

J Tuazon, J Peterson, M Priel - Proceedings of the third conference ..., 1988 - portal.acm.org

... microseconds. The paged memory management unit supports a demand-page virtual memory environment. An address translation and protection mechanism provides paging and **access control**. ... A rounding precision **control unit** does the ... and the hypercube **network**. ...

[Cited by 12](#) - [Related articles](#) - [All 2 versions](#)

Google 

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

network "access control" "control uni"

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google

Google scholar

tamper resistance "control unit" microchip OR

[Advanced Scholar Search](#)
[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

[Articles excluding patents ▾](#)
[1970](#) - [2000](#)
[include citations ▾](#)

Results **1 - 20** of about **39**. (0.21 sec)

Protecting smart cards from passive power analysis with detached power supplies

[oregonstate.edu \[PDF\]](#)

A Shamir - ... Hardware and Embedded Systems—CHES 2000, 2000 - Springer

... goal of an attacker is to extract these secret keys from the **tamper resistant** card in ... unnatural requirement, which increases the area and total power consumption of the **microprocessor**, and slows it ... The behaviour of the capacitors is defined by a simple switch **control unit** and four ...

[Cited by 108](#) - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

WIPP Transparency Project-container tracking and monitoring demonstration using ...

J SCHOENEMAN, HA SMARTT, D HOFER - 2000 - [osti.gov](#)

... the current GPS position data by the Communications **Control Unit** (CCU). ... licensing, designing the communications control and ground station **processing** units, procuring ... The system incorporates sensors, electronics, and **tamper-resistant** technologies, including encrypted and ...

[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 2 versions](#)

[PDF] The modular integrated video system(MIVS): A new generation of video ...

[iaea.org \[PDF\]](#)

KJ Gaertner, EW Dawes - IAEA Bulletin, 1990 - [iaea.org](#)

... MIVS is a **microprocessor**-controlled CCTV system designed to operate at 5-minute intervals in an ... The two basic components of the MIVS are the camera module and the recording **control unit**. Both are contained in **tamper-resistant** housings which are kept under Agency seals ...

[View as HTML](#) - [All 2 versions](#)

[CITATION] A Guide to Understanding Trusted Distribution in Trusted Systems

DD CSC - 1988 - Department of Defense Computer ...

[BOOK] Guide to Understanding Trusted Distribution in Trusted Systems

[google.com \[HTML\]](#)

JN Menendez, S Wright - 1988 - [books.google.com](#)

... For example, shrink wrapping products the size of mainframe central **processing** units (CPUs) or mainframe disk ... on the opening of a container and attaching the cable to a **control unit** via an ...

4.1.3 **Tamper-Resistant** Seals The use of **tamper-resistant** seals is another method of ...

[Related articles](#) - [All 5 versions](#)

[BOOK] Electronic access control

G Honey - 2000 - books.google.com

... and systems 69 4.1 Single door controllers and software 70 4.2 Multi-door systems and software
79 4.3 Central **processing** and personal ... It is possible to argue that a lock advertised as high
security should have features that offer more than ordinary **resistance** to picking ...

[Cited by 1](#) - [Related articles](#) - [All 3 versions](#)

[PDF] DEVELOPMENT OF INTEGRATED CONTAINMENT AND SURVEILLANCE ...

[jaea.go.jp \[PDF\]](#)

T MUKAIYAMA, H OGAWA, Y ... - JAERI- ..., 1998 - jolissrch-inter.tokai-sc.jaea.go.jp

... to **tamper resistance**. The serial bit stream is converted back to parallel form by similar centrally
located data transmission equipment at the **Control Unit** in the Safeguards Technology Laboratory
(SGL), and read in parallel into the CPU via the I/O bus. Central **processing** Unit ...

[Related articles](#)

[CITATION] Transaction Security System

JV Stevens - IBM SYSTEMS JOURNAL, 1991

[CITATION] Vehicle anti-theft system uses radio frequency identification

KS Khangura, NV Middleton, MM Ollivier, FM Co, MI ... - IEE Colloquium on Vehicle ..., 1993

[Cited by 5](#) - [Related articles](#) - [BL Direct](#)

Safeguards instrumentation: a computer-based catalog

LG Fishbone, B Keisch - 1981 - osti.gov

... 3 Secure Crane-Load Sensor 4 Ultrasonic Surveillance System 5 Valve **Tamper** Device 6 ... Monitor
78 NDA Reference Materials for Scrap and Waste 19 Portable **Microprocessor** bO Rocky ... Digital
Tiner (PI-200) 111 EURATOM TV System 112 Environnent-**Resistant** CCTV Canera ...

[Related articles](#) - [View as HTML](#)

[CITATION] Design Considerations and Features for a Nuclear Power Plant ...

JTK Mao, A Salvi - IEEE Transactions on Power Apparatus and Systems, 1981

[BOOK] Digital signal **processing** for multimedia systems

KK Parhi, T Nishitani - 1999 - books.google.com

... a cruise missile winds along hillsides searching for the target, the signal **processor** is busy ... 100K
multiply- add/s) performance advantage over the **microprocessor** of the day (M68000 ... Method
471 17.5 Robustness to Signal Transformations 472 17.6 **Tamper Resistance** 482 17.7 ...

[Cited by 49](#) - [Related articles](#) - [Library Search](#) - [All 3 versions](#)

National Information Systems Security (INFOSEC) Glossary

[dtic.mil \[PDF\]](#)

... /CENTRAL SECURITY SERVICE FORT GEORGE G ... - 2000 - oai.dtic.mil

... See TEMPEST. computer abuse Intentional or reckless misuse, alteration, disruption, or destruction of information **processing** resources. computer cryptography Use of a crypto-algorithm program by a computer to authenticate or encrypt/decrypt information. ...

[View as HTML](#)

[CITATION] Demand-side management alternatives

RM Delgado - Proceedings of the IEEE, 1985

[Cited by 9](#) - [Related articles](#) - [All 3 versions](#)

Technical Manual for Portable Duress Sensor

SONICRAFT INC CHICAGO IL - 1983 - oai.dtic.mil

... The batteries are located under an access cover which when removed trips a **tamper** switch. This causes the transmitter to transmit an alarm signal to the receiver. 1 1.2.3 RECEIVER ... Page 14. The receiver is designed to be **tamper** proof. If the antenna is discon- ...

[View as HTML](#)

[dtic.mil \[PDF\]](#)

[PDF] The WOSP model: Balanced information system design and evaluation

B Whitworth, M Zaic - Communications of the Association for Information ..., 1987 - CiteSeer

... Secure hardware is sealed and **tamper**-proof for this reason. ... Computer communication also requires common **processing**. ... An e-mail system's functionality is its ability to act as a word **processor**, and create message content. ...

[Cited by 21](#) - [Related articles](#) - [View as HTML](#) - [All 9 versions](#)

[psu.edu \[PDF\]](#)

Market survey results for alternate sensor communications

RR Rivas, KR White, LC Turnage - 1996 - osti.gov

... 16" X 14" X 4") 902-928 MHz; Spread spectrum Approach 1 Interfaces to normally-closed alarm contacts only No line protection; **tamper** switches in ... carrier Multiplexes up to 64 transmitters per panel Remote units receive as well as transmit enabling the central **control unit** to poll ...

[All 2 versions](#)

An applications guide to vehicle SNM monitors

PE Fehlau - Los Alamos National Lab. Report LA-10912-MS, 1987 - dx.doi.org

... Fig. 4. A lightweight hand-held monitor has the same basic elements as other vehicle SNM monitors. The radiation detector and **microprocessor** electronics are operated by rechargeable batteries. statistical increases, but they can also be caused by real intensity increases. ...

[Cited by 8](#) - [Related articles](#) - [All 2 versions](#)

[BOOK] Electronic Security Systems Pocket Book

G Honey - 1999 - books.google.com

... All devices closed Alarm open **Tamper** open Loop 4k 7 ii Loop 9k4 ii EOL plus parallel resistor

Loop OC Figure 1.6 Dual operation circuit For ... very often double up the supply conduc- tors since the greater the distance or the smaller the cable then the greater is the **resistance**. ...

[All 2 versions](#)


[\[PDF\] Data Security Kit](#)

SI Family - 2000 - [dsd.gov.au](#)

... March 6, 2001 1.1 Changed T.**TAMPER** and O.NO_TAMPER statements. Page 3. ... The TOE includes the Image **Control Unit** (ICU) and the hard disk drive. ... 2118 The DSK is a firmware enhancement to the digital image **processor** copiers in the form of ROMs. ...

[View as HTML](#) - [All 5 versions](#)

[dsd.gov.au \[PDF\]](#)

Google 

Result Page: 1 2 [Next](#)

tamper resistance "control unit" mic [Search](#)

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google

Google scholar

firewall "control unit" microchip OR microproce

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

Articles excluding patents ▾

1970

- 2001

include citations ▾

Results 1 - 20 of about 82. (0.80 sec)

A Web crawler design for data mining

M Theilwall - Journal of Information Science, 2001 - jis.sagepub.com

... The **processing** of the text of web pages in order to extract information can be expensive in terms of **processor** time. ... The distrib- uted crawler design described here can operate through this kind of **firewall** by hosting the **control unit** on a web server and coding all ...

[Cited by 92](#) - [Related articles](#) - [All 6 versions](#)

[psu.edu \[PDF\]](#)

[CITATION] FPGA-based Internet protocol firewall chip

A Kayssi, L Harik, R Ferzli, M Fawaz - ... , Circuits and Systems, 2000. ICECS 2000. The ..., 2000

[Cited by 9](#) - [Related articles](#) - [All 2 versions](#)

Architecture strategies for energy-efficient packet forwarding in wireless sensor ...

V Tsiatsis, S Zimbeck, M Srivastava - Proceedings of the 2001 ..., 2001 - portal.acm.org

... It exploits principles widely used in modern **firewall** network architectures and as our analysis shows achieves considerable energy savings. ... actually accepted by a node, and D SR is the delay of the serial port between the radio board and the main node **processor**. ... **Control Unit** ...

[Cited by 30](#) - [Related articles](#) - [All 8 versions](#)

[york.ac.uk \[PDF\]](#)

Router plugins: a software architecture for next-generation routers

D Decasper, Z Dittia, G Parulkar, B ... - IEEE/ACM Transactions ..., 2000 - portal.acm.org

... Our framework is also very well suited to application layer gateways (ALG's) and security devices like **firewalls**. ... core consists of a stream- lined IPv4/IPv6 implementation, which contains the (few) components required for packet **processing** that do ... Plugin **control unit**: The PCU ...

[Cited by 306](#) - [Related articles](#) - [BL Direct](#) - [All 37 versions](#)

[psu.edu \[PDF\]](#)

[CITATION] Synchronizing the IEEE 1149.1 test access port for chip leveltestability

D Bhavsar - IEEE Design & Test of Computers, 2000

[Cited by 3](#) - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

[psu.edu \[PDF\]](#)

[PDF] Using Parlay for centralised conferences in SIP environment

[iptel.org \[PDF\]](#)

RH Glitho, A Poulin, K Sylla - ... at the 7th International Conference on ..., 2001 - old.iptel.org
... The glue relies on the transaction API that provides capabilities for SIP message creation and
processing with a high level of ... 5.3 The MultiMedia **Control Unit** ... This approach can be used for
instance as replicator from multicast to unicast, and application-level filter in **firewalls**. ...

[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 3 versions](#)

[BOOK] Internetworking LANs and WANs

[tu-darmstadt.de \[PDF\]](#)

G Held - 1994 - ulb.tu-darmstadt.de
... repeaters 219 4.3.6 Bridge 220 4.3.7 Controlled Access Unit 221 4.3.8 Network **processor** 222
4.3 ... 9.2.1 Data flow 406 9.2.2 3270 protocols 407 9.2.3 Types of **control unit** 407 9.2 ... 10.2 **Firewall**
440 10.2.1 Placement 441 10.2.2 Features 442 Proxy services 442 Using classes 443 ...

[Cited by 12](#) - [Related articles](#) - [View as HTML](#) - [All 5 versions](#)

Globally Connected Fault-Tolerant Systems

P Thompson - Transputer and occam research: new directions: ..., 1993 - books.google.com
... **Control Unit** Link (" imhcure 3 ;:- o Crossbar Switch SeL. ... Figure 10: Processors are the Only
Firewalls Of course for the system to be resilient to the failure of a ... **Processor** failures can be
detected and reported by a fault-tolerant kernel as described in section 5. The second ...

[Cited by 8](#) - [Related articles](#) - [All 2 versions](#)

[CITATION] Expert computation on the iPSC™ concurrent computer

J Graham, J Rattner - Multiprocessors and array ..., 1987 - Society for Computer Simulation

[Cited by 2](#) - [Related articles](#)

Decentralized event correlation for intrusion detection

C Krügel, T Toth, C Kerer - Information Security and Cryptology—ICISC ..., 2001 - Springer
... alerts for the system administrator or to per- form active counter measures against an intruder
(eg harden the **firewall**). ... At least one host needs an installed **control unit**. ... Its task includes the
processing of attack specifications written in ASL and the con- figuration of the local ...

[Cited by 70](#) - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

[CITATION] Trace processors: Moving to fourth-generation microarchitectures

JE Smith, S Vajapeyam - Computer, 1997

[Cited by 86](#) - [Related articles](#) - [BL Direct](#) - [All 6 versions](#)

[CITATION] Economic online self-test in the time-triggered architecture

A Steininger, C Temple - IEEE Design & Test of Computers, 1999

[Cited by 12](#) - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)

[\[PDF\] ITU-T standardization activities for interactive multimedia communications on ...](#)

[uottawa.ca \[PDF\]](#)

J Toga, J Ott - Computer Networks, 1999 - beethoven.site.uottawa.ca

... Ø Multipoint **Processor** MP – audio and video mixing or switching, Ž . Ø Multipoint **Control Unit** MCU – contains MC, MP, and optionally a T.120 MCU, Ž . Ž GK – administrative control and "call routing", and Ø H.323 Proxy – controls how H.323 conferences may transit **firewalls**. ...

[Cited by 75](#) - [Related articles](#) - [View as HTML](#) - [All 10 versions](#)

[\[CITATION\] How does resource utilization affect fault tolerance?](#)

C Scherrer, A Steininger - IEEE International Symposium on Defect and Fault ..., 2000

[Cited by 3](#) - [Related articles](#) - [All 2 versions](#)

[\[CITATION\] Future architecture for flight control systems](#)

K Alstrom, J Torin - Digital Avionics Systems, 2001. DASC. The 20th ..., 2001

[Cited by 18](#) - [Related articles](#) - [BL Direct](#) - [All 6 versions](#)

[MultiMedia Digital Conferencing: A Web-enabled multimedia teleconferencing ...](#)

C Bisdikian, S Brady, YN Doganata, DA Fouiger, F ... - Journal of ..., 1998 - dx.doi.org

... cache (according to the push model described in the subsection on the Web **processor**). ...

Consequently, the MMDC client communicates with the MMDC server through **firewalls** in a ...

however, the audio channels are handled directly by the Multipoint **Control Unit**, as defined in ...

[Cited by 18](#) - [Related articles](#) - [BL Direct](#) - [All 9 versions](#)

[\[PDF\] Applying Mobile Agents to Intrusion Detection and Response.](#)

[psu.edu \[PDF\]](#)

WA Jansen, T Karygiannis, DG Marks - 2000 - Citeseer

... Transceivers perform command and control of locally running agents and the analysis or reduction **processing** of the information received from the agents. ... from multiple and distributed data sources such as **firewalls**, routers, and switches. ...

[Cited by 99](#) - [Related articles](#) - [View as HTML](#) - [All 47 versions](#)

[MMLite: a highly componentized system architecture](#)

[psu.edu \[PDF\]](#)

J Helander, A Forin - Proceedings of the 8th ACM SIGOPS ..., 1998 - portal.acm.org

... The **microprocessor** might be a DSP, a VLIW, or a micro-controller; the memory budget is severely ...

The virtual memory system implements **firewalls** between applications. ... A driver for the Interrupt

Control Unit (ICU) dispatches interrupts and keeps a registry of interrupt routines ...

[Cited by 84](#) - [Related articles](#) - [All 14 versions](#)

[\[PDF\] Remote Access Between COF-CC, SSCC, POIC and Other Decentralised ...](#)

[dlr.de \[PDF\]](#)

JP Fein - EUROPEAN SPACE AGENCY-PUBLICATIONS-ESA ..., 1996 - op.dlr.de

... ISS Digital Multipoint **Control Unit** Data and fax ESOC IKI ZUP ... V ide o backup External **Firewall**

E xterna l F irewall ... The raw data is passed to the telemetry **processor** where the housekeeping

data monitoring parameters are generated for display at the DLR/GSOC workstations. ...

[View as HTML](#) - [BL Direct](#) - [All 4 versions](#)

[Ulster biomedical engineering society \(TUBES\) proceedings of 12th annual ...](#)

AING CORRECTION - Irish Journal of Medical Science, 1999 - Springer

... composition by tailoring the sputter target stoichiometry and applying appropriate **processing** conditions. ... connect a clinical LAN to the Internet with a competent **firewall** between them ... physical units: the Biomedical Electronics Board (BEB) data acquisition and **control unit** and the ...

[Related articles](#)

Goooooogle ►

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [Next](#)

firewall "control unit" microchip OR r

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google

Google scholar

master slave firewall "control unit" microchip C

[Advanced Scholar Search](#)
[Scholar Preferences](#)

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

[Articles excluding patents ▾](#)
[1970](#) - [2001](#)
[include citations ▾](#)

Results **1 - 9** of **9**. (0.16 sec)

[PDF] [ITU-T standardization activities for interactive multimedia communications on ...](#)

[uottawa.ca \[PDF\]](#)

J Toga, J Ott - Computer Networks, 1999 - beethoven.site.uottawa.ca

... A Multipoint **Control Unit** MCU provides the ability to hold multiparty, multimedia conferences ... ensures that only valid H.323 traffic goes through the **firewall**. ... H.323 falls into four categories, the first three of which are mandatory for H.323 operation: Ø **Master-slave** determination: to ...

[Cited by 75](#) - [Related articles](#) - [View as HTML](#) - [All 10 versions](#)

[PDF] [Applying Mobile Agents to Intrusion Detection and Response.](#)

[psu.edu \[PDF\]](#)

WA Jansen, T Karygiannis, DG Marks - 2000 - Citeseer

... Transceivers perform command and control of locally running agents and the analysis or reduction **processing** of the information received from the agents. ... from multiple and distributed data sources such as **firewalls**, routers, and switches. ...

[Cited by 99](#) - [Related articles](#) - [View as HTML](#) - [All 47 versions](#)

[BOOK] [Real-time systems: design principles for distributed embedded ...](#)

H Kopetz - 1997 - books.google.com

... In the next step the raw data must be calibrated and transformed to standard measurement units. The term signal conditioning is used to refer to all the **processing** steps that are necessary to obtain meaningful measured data of an RT entity from the raw sensor data. ...

[Cited by 1145](#) - [Related articles](#) - [Library Search](#) - [BL Direct](#) - [All 2 versions](#)

[PDF] [Signaling Protocols for Internet Telephony](#)

[psu.edu \[PDF\]](#)

N Beijar - Helsinki University of Technology, 1998 - Citeseer

... 4.7.4 H.323 and **firewalls**.....39 4.8 OTHER FEATURES ... Backbone MC Multipoint Controller MCU Multipoint **Control Unit** MIME Multipurpose Internet Mail Extensions MP Multipoint **Processor** NIC Network ...

[Cited by 3](#) - [Related articles](#) - [View as HTML](#) - [All 7 versions](#)

[PDF] [General Packet Radio Service \(GPRS\): architecture, interfaces, and ...](#)

[gre.ac.uk \[PDF\]](#)

WCM Comput - 2001 - staffweb.cms.gre.ac.uk

... KEY WORDS packet **control unit** gateway GPRS support node general packet radio service GPRS ...
Most vendors developed SGSN based on existing multiple **processor** system products where ...
In the **master-slave** concept, a **master** PDCH accommodates PCCCHs to carry all ...

[Related articles](#) - [View as HTML](#) - [All 2 versions](#)

Analysis of Internet Telephony and the H. 323 Multimedia Standard

[dtic.mil \[PDF\]](#)

G Ficors, NAVAL POSTGRADUATE SCHOOL ... - 1999 - [oai.dtic.mil](#)

... in partial fulfillment of the requirements for the degree of **MASTER OF SCIENCE** ... Network LPC
Linear Predictive Vocoder MC Multipoint Controller MCU Multipoint **Control Unit** XIII ... Multimedia
Session Control Modem Modulator/demodulator MP Multipoint **Processor** PBN Packet ...

[All 3 versions](#)

[BOOK] Transactional agents: towards a robust multi-agent system

K Nagi - 2001 - [books.google.com](#)

... XIV Table of Contents 3.4.2 The Foundation of Intelligent Physical Agents 37 3.5 Conclusion
39 4 Overview of Transaction **Processing** 41 4.1 Transactions 41 4.1 Robustness of Transactions
42 4.3 Serializability 42 4.3.1 Theory 42 4.3.2 Equivalence of Histories and Schedules ...

[Cited by 19](#) - [Related articles](#) - [BL Direct](#) - [All 6 versions](#)

Substation Automation and Relay Communications

WJ ACKERMAN - Pilot protective relaying, 2000 - [books.google.com](#)

... urgent requirement because most SCADA systems in use, or even being specified today, are
incapable of receiving, **processing**, storing, using ... Circuit Breaker IBS!1 Control/Status Information-*
I Protective Relays Communication Line to **Master** Station RTU and I/O Equipment 3 ...

[BL Direct](#)

[PDF] IP MultiMedia over Satellite (IMMSAT)-Study Report

[psu.edu \[PDF\]](#)

RL Beachell, T Böhler, PD Holmes, E Ludvigsen, E ... - 2000 - [Citeseer](#)

Page 1. IP MultiMedia over Satellite (IMMSAT) - Study Report RAPPORT/REPORT Norwegian
Computing Center/Applied Research and Development Forward and return channels over
satellite with delay and BER H.320- H.323/SIP Gateway (GW) ...

[View as HTML](#) - [All 3 versions](#)

master slave firewall "control unit" m

